

ABSTRACT OF THE DISCLOSURE

A circuit simulating apparatus includes a netlist extracting unit extracting a netlist from circuit diagram data, an unnecessary circuit disconnecting unit forming a netlist with an unnecessary circuit

5 disconnected, from the netlist extracted by the netlist extracting unit, based
on an unnecessary circuit disconnecting terminal designated by an
unnecessary circuit disconnecting terminal designating unit, and a circuit
simulation unit performing a circuit simulation using a simulation input file
formed by using the netlist with the unnecessary circuit disconnected. As
10 the circuit simulation is performed using the simulation input file formed
from the netlist with the unnecessary circuit disconnected, the time
necessary for the circuit simulation can be reduced.